**REMARKS** 

Claims 1 - 14 are pending in the present application. By this Amendment, claim 7 has

been amended. No new matter has been added. It is respectfully submitted that this Amendment

is fully responsive to the Office Action dated September 14, 2006.

**Allowable Subject Matter:** 

Applicant gratefully acknowledges the indication that claims 8 and 10-14 have been

allowed.

In addition, Applicant also gratefully acknowledges the indication that claims 7 and 9

would be allowable once claim 7 is amended to correct a minor informality.

Accordingly, it is submitted that claim 7 has been amended to overcome the minor

informality, and therefore claims 7 and 9 are now allowable as well.

As to the Merits:

As to the merits of this case, the Examiner sets forth the following rejections:

1) claim 2 stands rejected under 35 USC §103(a) as being unpatentable over Page (U.S.

Patent No. 5,107,189, of record); and

2) claims 3-5 stand rejected over 35 USC §103(a) as being unpatentable over Ferreira

(U.S. Patent No. 4,701,787, of record).

Each of these rejections is respectfully traversed.

**Independent Claim 2:** 

With regard to independent claim 2, the Examiner specifically relies on col. 6, lines 30-44

of Page and asserts that such reference discloses a preamplifier circuit 37 and an output circuit 11,

wherein a level of the bias voltage that is applied by the preamplifier circuit 37 is changed in

accordance with contents of the video signal.

However, while the Examiner properly acknowledges, in the bridging paragraph between

pages 2 and 3 of the action, that, "Page does not disclose that the video signal can be of either

still image type or moving image type," the Examiner assets that "it would have been obvious to

one of ordinary skill in the art at the time of the when the invention was made that video signals

delivered to the video circuit can be of any well known type of a video signal, still and moving

types including."

Based on the Examiner's comments it appears that the Examiner has failed to appreciate,

for example, as discussed on page 9 in the present specification, that the level of the bias voltage

Vba that is applied by the preamplifier circuit 311 is set by the preamplifier control portion 314

to different amounts depending on whether the video signal relates to a still image or to a moving

image. For example, as discussed on page 9, lines 26-29, the bias voltage Vba is set to 1.0 volt

for a video signal relating to a still image and the bias voltage Vba is set to 0.4 volts for a video

signal relating to a moving image.

In other words, the Examiner has failed to appreciate that in the present claimed

invention, the level of the bias voltage that is applied by the preamplifier circuit is changed

depending on whether the video signal is related to a still image or to a moving image.

As such, it is submitted that the Examiner has clearly failed to establish a prima facie case

of obviousness, since the Examiner has failed to properly consider the features of claim 2

concerning the level of the bias voltage is changed in accordance with the video signal that is

either a video signal related to a still image or a video signal related to a moving image.

Moreover, it is submitted neither <u>Page</u> nor <u>Ferreira</u> discloses or fairly suggests that "the

level of the bias voltage is changed in accordance with the video signal that is either a video

signal related to a still image or a video signal related to a moving image".

**Independent Claim 3:** 

With regard to claim 3, the Examiner asserts that Ferreira discloses a preamplifier circuit

TR1: an output circuit TR12 for amplifying power of the signal that is delivered from the

preamplifier circuit through resistor R44; and a variable bias circuit (diodes Dl-D4 and

potentiometer P2) for changing a level of the bias voltage that is added in the preamplifier circuit

TR1.

However, while the Examiner properly acknowledges in page 3, lines 11-14 of the action

that, "Ferreora does not disclose that the video signal can be of either still image type or moving

image type," the Examiner assets that "it would have been obvious to one of ordinary skill in the

art at the time of the when the invention was made that video signals delivered to the video

circuit can be of any well known type of a video signal, still and moving types including."

Again, as discussed above, it appears that the Examiner has failed to appreciate that in the

present claimed invention, the level of the bias voltage that is applied by the preamplifier circuit

is changed depending on whether the video signal is related to a still image or to a moving image.

As such, it is submitted that the Examiner has clearly failed to establish a prima facie case

of obviousness, since the Examiner has failed to properly consider the features of claim 3

concerning a variable bias circuit for changing a level of the bias voltage that is added in the

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preamplifier circuit in accordance with the video signal that is either a video signal related to a

still image or a video signal related to a moving image.

Moreover, it is submitted that neither Page nor Ferreira discloses or fairly suggests that

"the level of the bias voltage is changed in accordance with the video signal that is either a video

signal related to a still image or a video signal related to a moving image".

In view of the aforementioned amendments and accompanying remarks, Applicant

submits that the claims, as herein amended, are in condition for allowance. Applicant requests

such action at an early date.

If the Examiner believes that this application is not now in condition for allowance, the

Examiner is requested to contact Applicant's undersigned attorney to arrange for an interview to

expedite the disposition of this case.

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If this paper is not timely filed, Applicants respectfully petition for an appropriate extension of time. The fees for such an extension or any other fees that may be due with respect to this paper may be charged to Deposit Account No. 50-2866.

Respectfully submitted,

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